

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of	:	Attorney Docket No. 2006_0223A
Toshiaki TAKENAKA et al.	:	<b>Confirmation No. 3496</b>
Serial No. 10/595,157	:	Group Art Unit 1791
Filed March 9, 2006	:	Examiner John L. Goff, II
METHOD OF MANUFACTURING MULTI-LAYER CIRCUIT BOARD	:	<b>Mail Stop: AF</b>

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**AFTER FINAL AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the final Office Action mailed December 30, 2009, please amend the  
above-identified application as follows: